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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10062426	FILING DATE 02/05/2002	CLASS 257	SUBCLASS 736	GAU 2811	EXAMINER PAREKH.
<b>**APPLICANTS:</b> Yamane Tae;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				OKI.302	
Verified and Acknowledged Examiners's initials					
TITLE : Chip-size semiconductor package					

U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
Assistant Examiner		<b>DRAWING</b>	
<b>ISSUE FEE</b>		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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